



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re the Application of:

Serge Lasserre (TI-31354)

Art Unit: 2188

Technology Center 2100

Serial No: 09/932,381

Examiner: Reginald G. Bragdon

Filed: August 17, 2001

Conf. No.: 4434

For: Local Memory With Indicator Bits to Support Concurrent DMA and CPU Access

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicant brings the reference listed on the enclosed PTO/SB/08A to the attention of the Patent and Trademark Office in connection with this application.

This Information Disclosure Statement is being submitted with a Request for Continued Prosecution of this application. As such, this Information Disclosure Statement is timely.¹

A copy of the reference is enclosed. It is in the English language. As such, no additional statement of relevance is provided in this paper.²

By citing these references, Applicant does not admit that this reference is, or is considered to be, material to the patentability of any of the claims of this application.³

¹ 37 C.F.R. §1.97(b)(4).

² 37 C.F.R. §1.98(3)(i).

³ 37 C.F.R. §1.97(h).

Consideration of this information this application is respectfully requested.

Respectfully submitted,



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CERTIFICATE OF MAILING

37 C.F.R. 1.8

The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

Commissioner for Patents
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on March 10, 2004.



Rodney M. Anderson
Registry No. 31,939